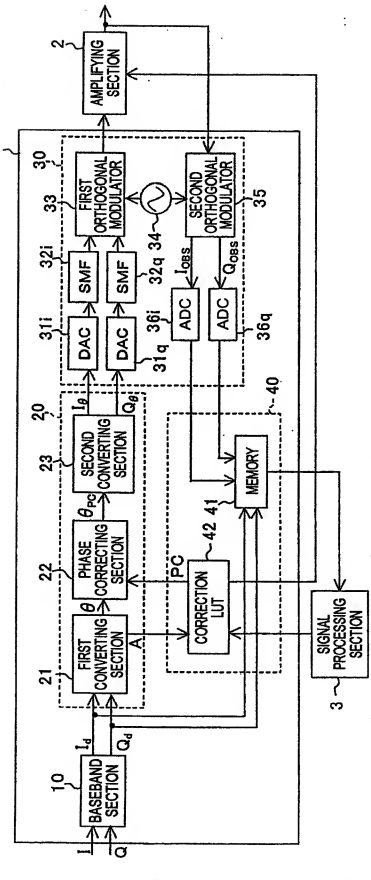
FIG. 1


1
2
AMPLIFYING SECTION
3
SIGNAL PROCESSING SECTION
10
BASEBAND SECTION
I
Q
I_d
Q_d
20
21
FIRST CONVERTING SECTION
A
22
PHASE CORRECTING SECTION
θ
23
SECOND CONVERTING SECTION
θ_{PC}
I_θ
Q_θ
30
31i
DAC
31q
DAC
32i
SMF
32q
SMF
33
FIRST ORTHOGONAL MODULATOR
34
35
SECOND ORTHOGONAL MODULATOR
I_{OBS}
Q_{OBS}
36i
ADC
36q
ADC
40
41
MEMORY
PC
42
CORRECTION LUT